

PROJETO DE SISTEMAS EM CHIP		Carga Horária(h)			
		TIPO	TEÓRICA	PRÁTICA	TOTAL
		Semanal	4	0	4
Semestra I	68	0	68		
Caráter: Optativa	Código:	Período: Módulo VIII e IX		Oferta: IGE	
Ementa: Desenvolvimento de sistemas em chips (SoC) incorporando prioritariamente funções de telecomunicações e redes de computadores.					
Objetivos: Compreender o processo de projeto de sistemas integrados em chip.					
Bibliografia Básica: <ul style="list-style-type: none"> • REIS, RICARDO; LUBASZEWSKI, MARCELO; JESS, JOCHEN. Design of systems on a chip: design and test. Boston: Springer, 2006. • PASRICHA, SUDEEP; DUTT, NIKIL; KAUFMANN, MORGAN. On-Chip communication architectures: system on chip Interconnect. Boston: Morgan Kaufmann. 2008. • HERLIHY, MAURICE; SHAVIT, NIR; KAUFMANN, MORGAN. The Art of multiprocessor programming. Oxford: Elsevier Science, 2008. 					
Bibliografia Complementar: <ul style="list-style-type: none"> • SCHAUB, Keith, B. Production testing of RF and system-on-a-chip devices for wireless communications. Boston: Artech House. 2004. • KELLY, JOE; ENGELHARDT, MICHAEL D. Advanced production testing of RF, SoC, and SiP Devices. Boston: Artech House, 2006. • FREVERT, RONNY. Modeling and simulation for RF system design. Boston: Springer. 2005. • GU, QIZHENG. RF System Design of Transceivers for Wireless Communications. New York: Springer, 2006. • MURALI, SRINIVASAN. Designing reliable and efficient networks on chips. New York: Springer. 2009. 					